

<b>Title: Junior electrical engineer : Electrical Circuit Analysis</b>		<b>CHD-042</b>
<b>REPORTS TO LINE MANAGER: Head of H &amp; CD Division; Department of CODAC&amp;IT, H&amp;CD and Diagnostics</b>		
<b>GRADE RANGE: G4-G5</b>		
<b>Date Written: 06/07/2007</b>	<b>Date Revised:</b>	<b>Date Revised:</b>

**Purpose:** Support the division activities in all matter relating to the analysis of electrical circuit for the PS of the NB, IC, EC and LH systems. Work with the division to support port integration of the PS and contribute to the definition of the technical specifications. Interface between various activities and analysis work done by the DAs involved in the procurement activities of H & CD systems.

**Major Duties/Responsibilities:**

- To be responsible for circuit analysis of the PS of the competence of H&CD
- To include in this work the interfaces and coupling with other systems
- To define tasks and oversee the design and R&D work undertaken by the Participant Teams on this field of work.
- To review and maintain the Project Integration documents referring to these WBS items.
- Wall conditioning through ICH and other RF techniques
- Assist with the preparation of the relevant procurement packages.
- Collaborate to the activities of the project related to H&CD.
- Assists with the preparation of the interface documentation and keeping it up to date.
- Assist with the preparations for the installation of the H & CD systems on ITER.
- Report variances on all technical, cost and schedule aspects immediately
- Support effective risk identification and management.
- Show strong commitment to the ITER safety program and enforces it through individual behavior and in his organization.
- Maintain a strong commitment to the implementation and perpetuation of ITER values and ethics.

**Qualifications Required:**

- The candidate should be a junior electrical engineer with relevant experience of at least 1-3 years.
- The candidate should have experience of technical design. and
- The candidates must be fluent in English (speaking and writing)
- The candidate must possess basic knowledge of the ITER project
- Experience in effective QA management and implementation would be an advantage.

**Work Direction and Interfaces:**

Report to the DH.

**Authority/Approval Levels:**

Has authority and approval levels defined by the management for his scope of work.

**Measures of Effectiveness:**

Successfully accomplish the tasks above.